

### APEC Conference and Expo 2012

Full-Duplex Digital Isolator for Fast Bi-Directional Communication Across a High-Withstand Barrier

Presenter: Bill Morong, PE

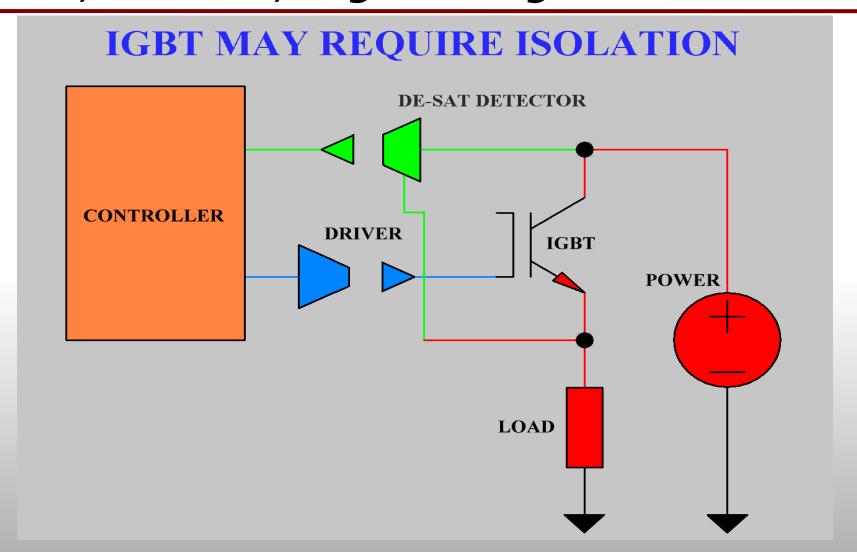


### Requirements for IGBT Drivers

# **IGBT REQUIRES BI-DIRECTIONAL DATA DE-SAT DETECTOR CONTROLLER DRIVER IGBT POWER LOAD**

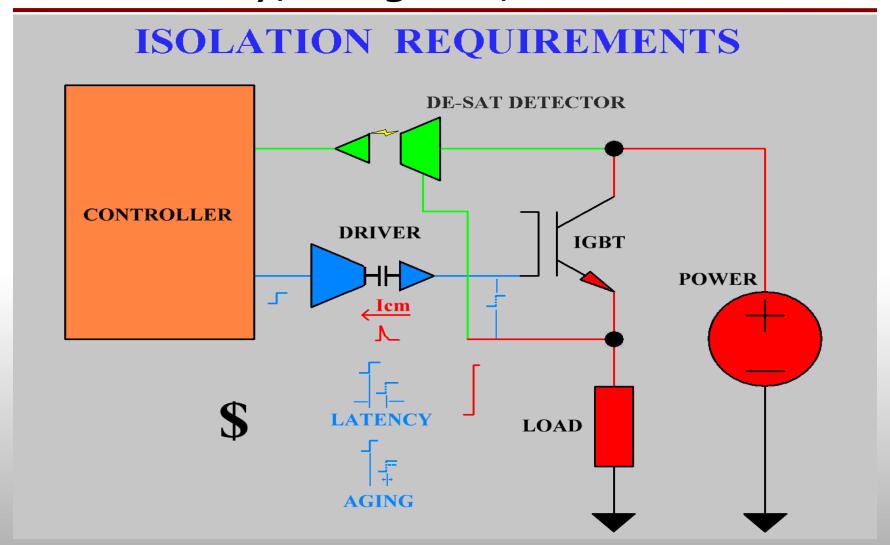


### Fast, Reliable, High-Voltage Isolation





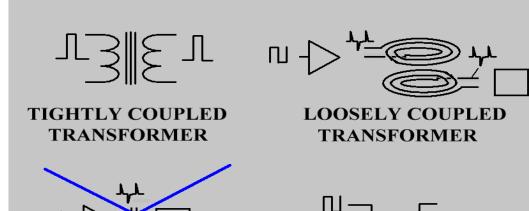
### Low-Latency, Long Life, Low Cost





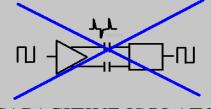
### Choices Limited by DV/DT Requirements

#### **ISOLATION METHODS**





**GMR ISOLATOR** 







**CAPACITIVE ISOLATOR** 

**OPTO-COUPLER** 

FIBER-OPTIC LINK

TIGHTLY-COUPLED XFMR MAY BE CAPACITIVE AND COSTLY UNLESS PULSED

LOOSELY-COUPLED XFMR MAY EMIT NOISE

**GMR ISOLATION NOT YET COMMON FOR IGBT DRIVERS** 

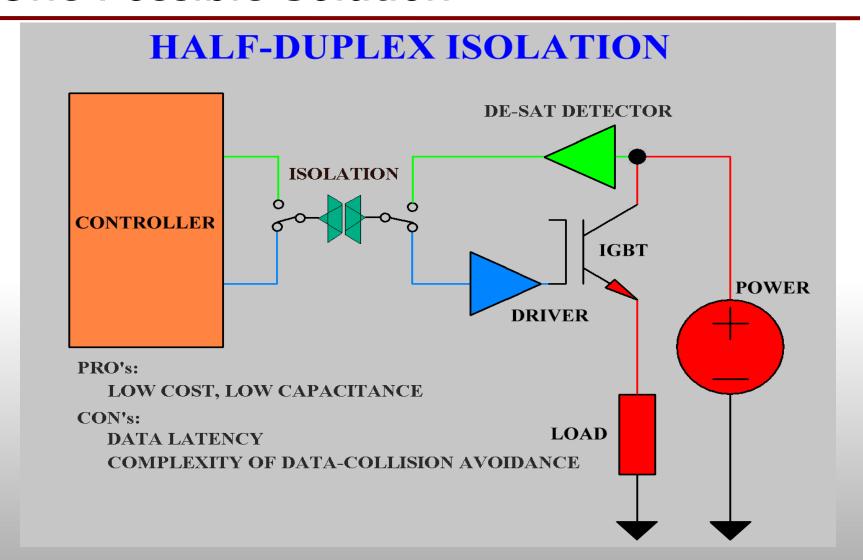
CAPACITIVE TOO DIFFICULT AT HIGH CM dV/dT

**OPTO-COUPLERS KNOWN FOR AGING** 

FIBER OPTICS COSTLY

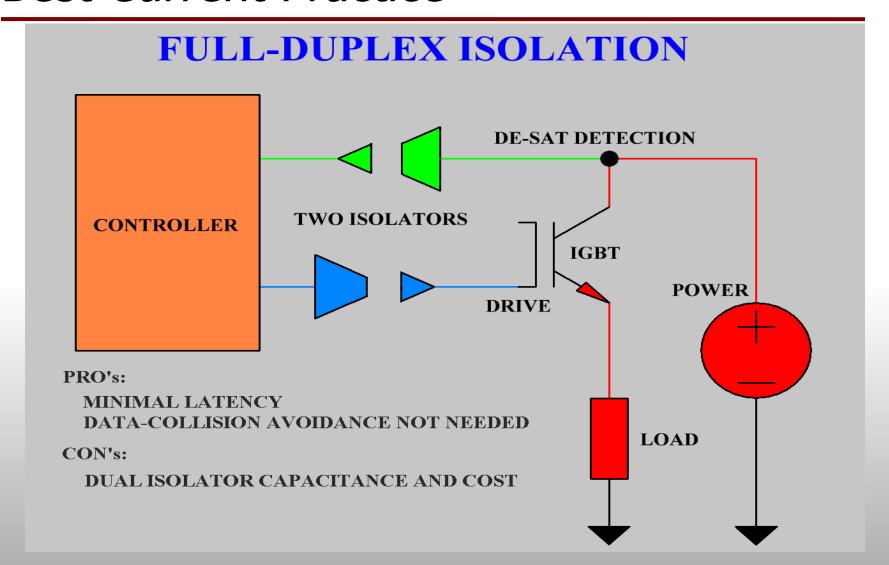


#### One Possible Solution





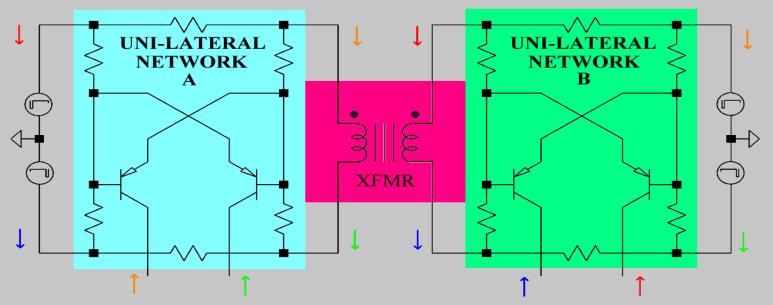
#### Best Current Practice





### Patent-Pending Digital Isolator

## COGNIPOWER FULL-DUPLEX SINGLE-TRANSFORMER ISOLATION



#### PRO's:

SINGLE TRANSFORMER CAPACITANCE
SINGLE TRANSFORMER COST
DATA COLLISION CIRCUITRY NOT NEEDED
FULL-DUPLEX DATA PROPAGATION MINIMIZES DATA LATENCY



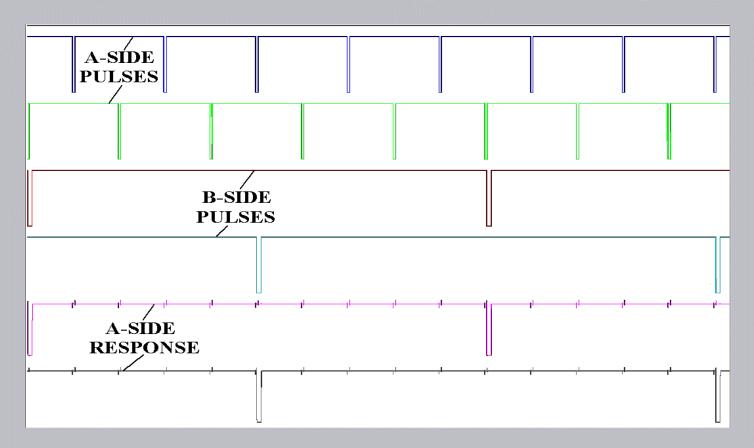
### The Key to the Technique

### **UNI-LATERAL NETWORK** 1000 464 3|||[5] 464 1000 24.9 REJECTS DATA FROM GENERATORS RESPONDS TO DATA FROM TRANSFORMER 1000 1000



#### Pulse Waveforms in SPICE

#### **UNI-LATERAL NETWORK REJECTION**

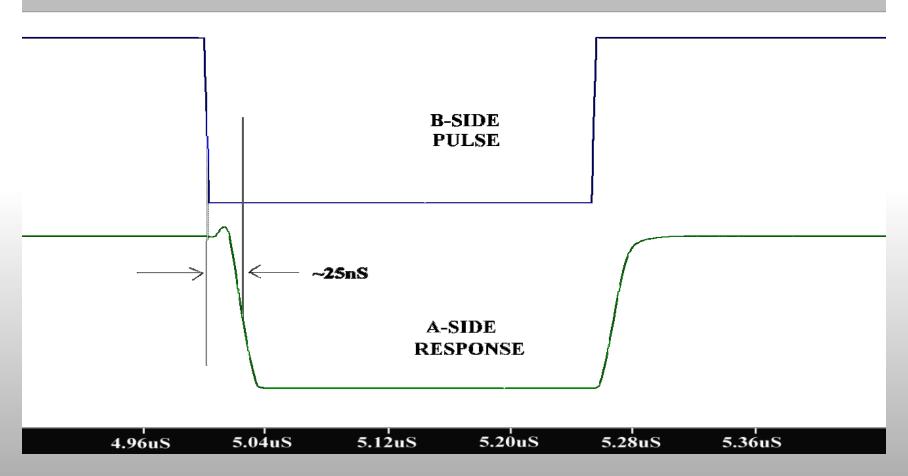


DATA PULSES TRANSMITTED TO REDUCE TRANSFORMER SIZE/CAPACITANCE



### Latency of 25 nS

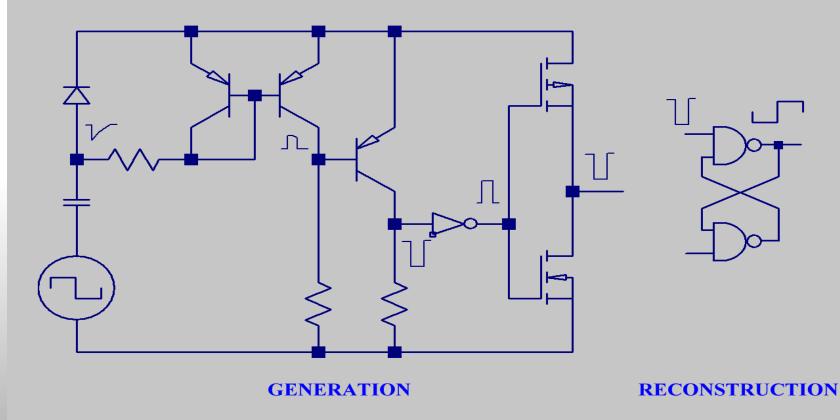
#### **ISOLATION PROPAGATION DELAY**





### Simple Data Reconstruction

# DATA PULSE GENERATION DATA RECONSTRUCTION

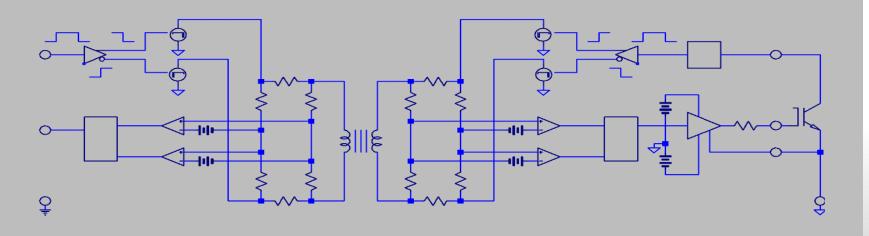




### IGBT Driver Application

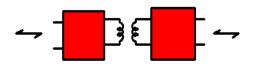
#### **COGNI POWER**

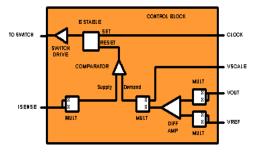
## IGBT DRIVER WITH SINGLE-TRANSFORMER FULL-DUPLEX DATA TRANSMISSION

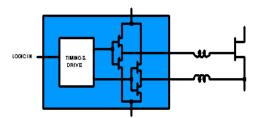




### **Building Blocks**









Digital Isolator APEC 2012

Predictive Energy
Balancing Controls
APEC 2011

Common Mode Switch Drive APEC 2010

Single-Stage PFC And Regulation APEC 2013?

Arbitrarily high voltage withstand

Harmonically pure
Agile response for
any SMPC topology

Precise, low-latency high-side drive for FETs and IGBTs

Ask about our AC-DC converter design



### Questions?

### Come see us at Booth # 1007

Thank you

